# **HCP0704**

## High current power inductors



#### **Product features**

- 6.8 x 6.8 x 4.2 mm surface mount package
- Iron powder core material
- Magnetically shielded, low EMI
- High temperature core material eliminates thermal aging issues
- High current carrying capacity, low core losses
- Tight DCR tolerance for sensing circuits
- Inductance range from 0.40 μH to 4.7 μH
- Current range from 5.0 A to 27 A
- Frequency range up to 2 MHz
- Halogen free, lead free, RoHS compliant

#### **Applications**

- Voltage Regulator Module (VRM)
- · Multi-phase regulators
- · Desktop and servers
- · Base station equipment
- Notebook and laptop regulators
- · Data networking and storage systems
- · Point-of-load modules (POL)
- Battery power systems
- · DCR sensing circuits

#### **Environmental Data**

- Storage temperature range (Component): -40 °C to +155 °C
- Operating temperature range: -40 °C to +155 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant









#### **Product Specifications**

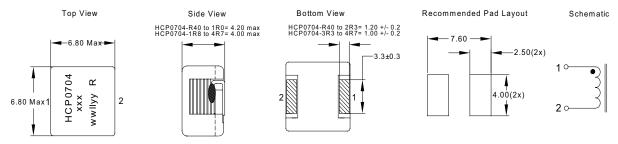
OCL1 ± 25% (μH)	FLL <sup>2</sup> Min. (μH)	I <sub>rms</sub> ³ (A)	I <sub>sat</sub> <sup>4</sup> @ +25 °C (A)	DCR (mΩ) @ +20 °C	K-factor⁵
0.40	0.28	17	27	3.2 ±10%	383.1
0.60	0.42	14	21	4.5 ±10%	313.5
1.00	0.7	12	17	6.2 ±10%	265.3
1.80	1.26	8.5	13	11.0 ±10%	202.8
2.30	1.56	7.5	11.5	16.5 ±10%	164.2
3.30	2.31	6.0	9.5	25.0 ±10%	149.9
4.70	3.29	5.0	8.0	29.5 ±10%	127.7
	0.40 0.60 1.00 1.80 2.30 3.30	0.40     0.28       0.60     0.42       1.00     0.7       1.80     1.26       2.30     1.56       3.30     2.31	0.40     0.28     17       0.60     0.42     14       1.00     0.7     12       1.80     1.26     8.5       2.30     1.56     7.5       3.30     2.31     6.0	0.40     0.28     17     27       0.60     0.42     14     21       1.00     0.7     12     17       1.80     1.26     8.5     13       2.30     1.56     7.5     11.5       3.30     2.31     6.0     9.5	

- 1 Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 0.10 V<sub>rms</sub>, 0.0 Adc
- 2 Full Load Inductance (FLL) Test Parameters: 100 kHz, 0.1  $V_{rms}$ ,  $I_{sat}$ 1
- 3 I<sub>rms</sub>: DC current for an approximate temperature rise of 40 °C without core loss. Derating
- is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed +125 °C under worst case operating conditions verified in the end application.
- 4  $I_{\text{Sat}}$ : Peak current for approximately 20% rolloff at +25 °C.
- 5 K-factor: Used to determine Bp-p for core loss (see graph). Bp-p = K \* L \*  $\Delta$ I : (Gauss), K: (K-factor from table), L: (inductance in µH), AI (peak-to-peak ripple current in amps).

  6 Part Number Definition: HCP0704-xxx-R
- HCP0704 = Product code and size
- xxx= Inductance value in  $\mu\text{H, R} =$  decimal point. If no "R" is present, then third character = # of zeros

  • "-R" suffix = RoHS compliant

#### **Dimensions (mm)**



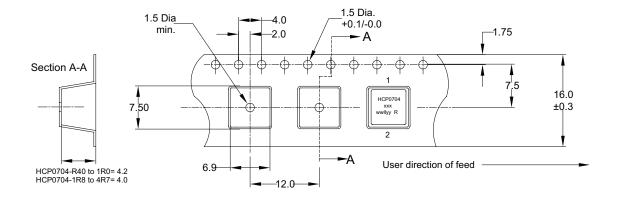
The nominal DCR test point is in the middle of the terminal

xxx =Inductance value in  $\mu$ H. (R = Decimal point). If no "R" is present, then last character is # of zeros Part Marking: HCP0704 wwllyy = Date code R = Revision level

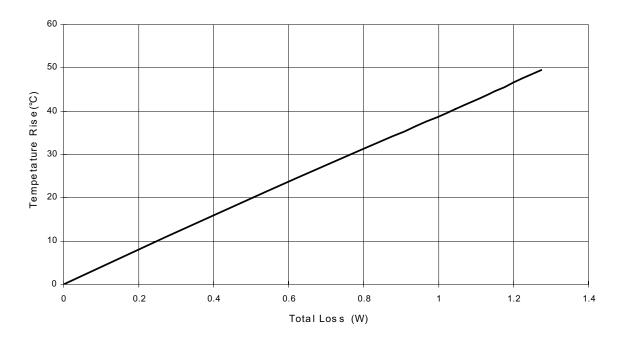
Do not route traces or vias underneath the inductor

#### Packaging information (mm)

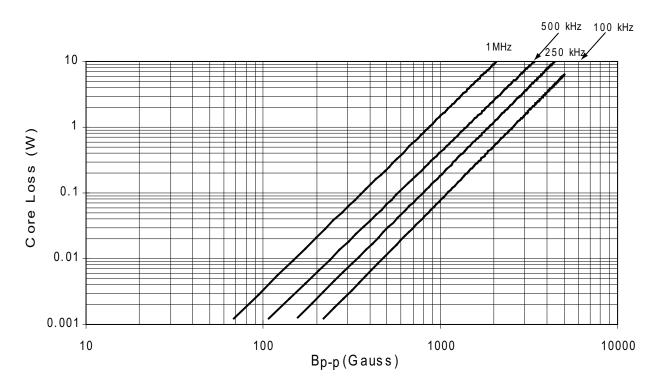
Supplied in tape and reel packaging, 1000 parts per 13" diameter reel.



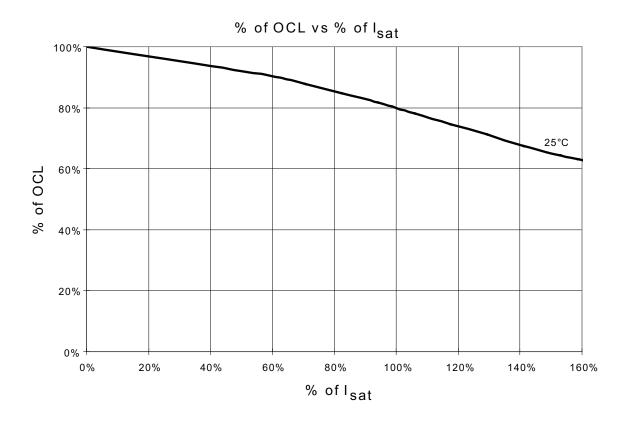
#### Temperature rise vs. total loss



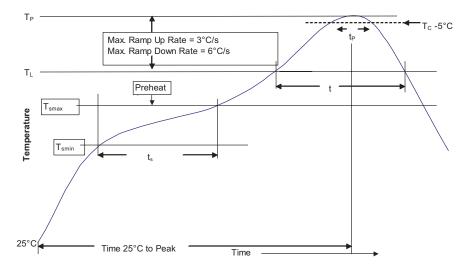
### Core loss vs. B<sub>p-p</sub>



#### **Inductance characteristics**



#### Solder reflow profile



-<sub>Tc-5°C</sub> Table 1 - Standard SnPb Solder (T<sub>C</sub>)

Package Thickness	Volume mm3 <350	Volume mm3 ≥350
<2.5mm)	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (T<sub>C</sub>)

Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

#### **Reference JDEC J-STD-020**

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak • Temperature min. (T <sub>smin</sub> )	100°C	150°C
• Temperature max. (T <sub>smax</sub> )	150°C	200°C
• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds
Average ramp up rate $T_{smax}$ to $T_p$	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperature (TL) Time at liquidous (tL)	183°C 60-150 Seconds	217°C 60-150 Seconds
Peak package body temperature (Tp)*	Table 1	Table 2
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$	20 Seconds**	30 Seconds**
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature	6 Minutes Max.	8 Minutes Max.

 $<sup>^{*}</sup>$  Tolerance for peak profile temperature (T<sub>p</sub>) is defined as a supplier minimum and a user maximum.

Life Support Policy: Eaton does not authorize the use of any of its products for use in life support devices or systems without the express written approval of an officer of the Company. Life support systems are devices which support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

Eaton reserves the right, without notice, to change design or construction of any products and to discontinue or limit distribution of any products. Eaton also reserves the right to change or update, without notice, any technical information contained in this bulletin.

#### Eaton Electronics Division

1000 Eaton Boulevard Cleveland, OH 44122 United States www.eaton.com/electronics

© 2017 Eaton All Rights Reserved Printed in USA Publication No. 4348 BU-SB09086 July 2017



<sup>\*\*</sup> Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.